

General News

- Scrubbing Indico Pages
 - Tile+LAr: https://indico.bnl.gov/conferenceDisplay.py?confld=1416 (Oct.22-23)
 - ITk: https://indico.bnl.gov/conferenceDisplay.py?confld=1523 (Nov.5-6)
 - TDAQ+Muon: https://indico.bnl.gov/conferenceDisplay.py?confld=1524 (Nov.23-24)
- Director's Review dates changed: now Jan. 20-22, 2016
- Global Issues from ITK Scrubbing last week
 - US System Integration Engineer for each system
 - ITK-electronics, mechanics, LAr, Tile, Muon, TDAQ
 - ~0.2 FTE / year costed in Phase-II Management section
 - Infrastructure: encouraging institute contributions
 - o remind that US ATLAS purchased items revert to BNL at the end of the project
- Pre-Construction vs Construction Budgets
 - should separate these clearly in cost estimates & schedules
 - o separate tabs in your detailed budget spreadsheets?
- Travel Funds on Project
 - cannot be used for off-project (physicist) personnel



D.R. Milestones

- Upcoming deadlines in Director's Review Preparation
 - http://pages.iu.edu/~hgevans/atlas/admin/protected/upgrade-log/151027_cdr-docs.html
 - user=atlas; pwd=atlas-fyeo

Item		Complete Draft	Responsible	Phase-I examples (<u>docDB #</u>) • Phase-II template/instructions
Detailed WBS/Budget/Schedule • basis for all documents		Tile:12-Nov LAr:12-Nov ITk:19-Nov Muon:30-Nov TDAQ:30-Nov	L2s	template (modify as needed)
WBS Dictionary ● at L4		Tile:12-Nov LAr:12-Nov ITk:19-Nov Muon:30-Nov TDAQ:30-Nov	L2s	77 ■ template
BoEs format for cost/effort tables format for risk reporting		04-Dec	Inst Reps + L2s	220-248, 249,263-275,289,337-339,364 250-257 • template
Cost Book		07-Dec	PO	155-157, 172
Milestones • needed at CERN • start/end of phases • external dependencies		07-Dec	L2s	119,149,151,152 • template

also use to construct simplified timeline charts



BoE Status

- BoEs for each L4 Deliverable
 - include contribution from multiple institutes to that Deliverable

System	L4 Deliv's	BoE Drafts Received
Pixels	7	some of 6.1.y.4(local supports) & 6.1.y.5(modules)
Strips	3	6.2.y.1(stave cores), 6.2.y.2(rdout chips, 6.2.y.3(modules)
ITK Com	5	?
LAr	5	6.4.1.1,2,3(sFCAL) & some of 6.4.y.2(FEB)
Tile	4	6.5.1.1(main board)
Muon	7	requests out
TDAQ	8?	requests out

updated L4 Deliverable counts/BoE drafts after the meeting